

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Express Mail No.: EV 351237171 US.

Applicant : Alexander G. MacInnis, et al.
Application No. : To be assigned
Filed : January 21, 2004
Title : GRAPHICS DISPLAY SYSTEM WITH WINDOW DESCRIPTORS

Grp./Div. : To be assigned
Examiner : To be assigned

Docket No. : 51706/JEJ/B600

INFORMATION DISCLOSURE STATEMENT
FOR A CONTINUING NON-CPA APPLICATION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Post Office Box 7068
Pasadena, CA 91109-7068
January 21, 2004

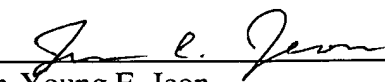
Commissioner:

In compliance with the duty of disclosure under 37 CFR §§ 1.56, 1.97 and 1.98, enclosed is FORM PTO/SB/08A/B. The references listed thereon are not enclosed because they were of record in a parent application, Application No. 09/437,716, filed November 9, 1999.

It is respectfully requested that these references be considered in the examination of this application, and identified in the list of references cited on the patent issuing on this application.

Respectfully submitted,

CHRISTIE, PARKER & HALE, LLP

By 
Jun-Young E. Jeon
Reg. No. 43,693
626/795-9900

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Enclosure(s)
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FORM PTO-1449	Attorney Docket Number	51706/JEJ/B600
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)	Application Number	To be assigned
	Filing Date	January 21, 2004
	Applicant(s)	Alexander G. MacInnis, et al.
	Group Art Unit	To be assigned
	Examiner Name	To be assigned

U.S. PATENT DOCUMENTS						
EXAMINER INITIALS	DOCUMENT NUMBER	ISSUE DATE	PATENTEE	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	5,155,816	10/1992	Kohn	395	375	
	5,604,514	02/1997	Hancock	345	154	
	5,758,177	05/1998	Gulick et al	395	800.01	
	5,864,345	01/1999	Wickstrom et al.	345	431	
	5,949,439	09/1999	Ben-Yoseph et al.	345	503	
	5,956,041	09/1999	Koyamada et al	345	420	

FOREIGN PATENT DOCUMENTS							
EXAMINER INITIALS	DOCUMENT NUMBER	PUBLICATION DATE	COUNTRY OR PATENT OFFICE	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

OTHER DOCUMENTS	
EXAMINER INITIALS	Include name of the author (in CAPITAL LETTERS), title of the article, title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
	POWER TV, INC., Eagle TM Graphics/Audio Media Compositor, Version 1.7, February 27, 1997

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	MOTOROLA, INC., MC92100 "Scorpion" Graphics Display Generator, SDRAM Controller, and Digital Video Encoder, 1997, 6 pages

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•	5,434,957	07/1995	Moller	395	131	
•	6,023,302	02/2000	MacInnis et al	348	597	
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•	BLINN, Jim; "Jim Blinn's Corner Dirty Pixels; publication; 1998; pp. 179-190; Chapter Sixteen, Compositing-Theory, September 1994; Morgan Kaufman Publishers, Inc.; San Francisco, CA; U.S.A.

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		4,020,332	04-26-1977	Crochiere et al.
		4,679,040	07-07-1987	Yan
		5,097,257	03-17-1992	Clough et al.
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		5,696,527	12-09-1997	King et al.
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		6,121,978	09-19-2000	Miler

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		6,151,030	11-21-2000	DeLeeuw et al.
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	-	EP 0752695 A2	01-08-1997	Sun Microsystems, Inc.	
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	•	WO 00/28518	05-18- 2000	Broadcom Corporation	

OTHER DOCUMENTS		
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	•	SUN, HUIFANG et al., "A New Approach for Memory Efficient ATV Decoding," 1997 IEEE International Conference on Consumer Electronics, Pages 174-175, Los Angeles, 1997.
	•	FOLEY, JAMES D. et al., "Computer Graphics: Principles and Practice," 4 Pages, Second Edition in C, Addison-Wesley Publishing Company, USA, 1996.
	•	BAO, JAY et al., "HDTV Down-Conversion Decoder," IEEE Transactions on Consumer Electronics, Pages 402-410, Volume 42, No. 3, August 1996.
	•	MOKRY, ROBERT et al., "Minimal Error Drift in Frequency Scalability for Motion-Compensated DCT Coding," IEEE Transactions on Circuits and Systems for Video Technology, Pages 392-406, Volume 4, No. 4, August 1994.
	•	VETRO, ANTHONY et al., "Minimum Drift Architectures for 3-Layer Scalable DTV Decoding," IEEE Transactions on Consumer Electronics, Pages 527-536, Volume 44, No. 3, August 1998
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	•	SUN, HUIFANG, "Hierarchical Decoder For MPEG Compressed Video Data," IEEE Transactions on Consumer Electronics, Pages 559-564, Volume 39, No. 3, August 1993.
	•	YU, HAOPING et al., "Block-Based Image Processor for Memory Efficient MPEG Video Decoding," 1999 IEEE International Conference on Consumer Electronics, Pages 114-115, 1999.

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		4,532,547	07-30-1985	Bennett
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		EP 0746116 A2	12-04-1996	Mitsubishi Denki Kabushiki Kaisha Tokyo 100 (JP)	

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FORM PTO/SB/08A/B (10-01) Substitute for PTO-1449A/B	Attorney Docket Number	51706/JEJ/B600
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)	Application Number	To be assigned
	Filing Date	January 21, 2004
	Applicant(s)	Alexander G. MacInnis, et al.
	Group Art Unit	To be assigned
	Examiner Name	To be assigned

U.S. PATENT DOCUMENTS				
EXAMINER INITIALS	Cite No. ¹	<u>DOCUMENT NUMBER</u> Number - kind code ² . (If known)	PUBLICATION DATE MM-DD-YYYY	NAME OF PATENTEE
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		GB 2287627 A	09-20-1995	VTECH Electronics Ltd	

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OTHER DOCUMENTS		
EXAMINER INITIALS	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article, title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.

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OTHER DOCUMENTS		
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EXAMINER INITIALS	Cite No. ¹	Foreign Patent Document Country Code ³ - Number ⁴ - Kind Code ⁵ (If known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	T ⁶ (✓)

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